OCENME

September, 2013

SJ-FET

OSD5N60S/OSU5N60S
600V N-Channel MOSFET

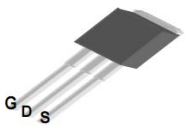
Description

SJ-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

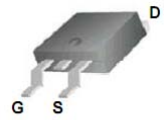
This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. SJ-FET is suitable for various AC/DC power conversion inswitching mode operation for higher efficiency.

Features

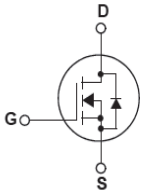
- 600V @TJ = 150 ℃
- Typ. RDS(on) = 0.77 Ω
- Ultra Low Gate Charge (typ. Qg =15nC)
- 100% avalanche tested
- Rohs Compliant



I-PAK (TO-251)



D-PAK (TO-252)



Absolute Maximum Ratings

Symbol	Parameter	OSD5N60S	OSU5N60S	Unit
V_{DSS}	Drain-Source Voltage	600		V
I_D	Drain Current -Continuous (TC = 25℃) -Continuous (TC = 100℃)	5* 4.5*	5 4.5	A
I_{DM}	Drain Current - Pulsed (Note 1)	20*	20	A
V_{GSS}	Gate-Source voltage	±30		V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	67.5		mJ
I_{AR}	Avalanche Current (Note 1)	1		A
E_{AR}	Repetitive Avalanche Energy (Note 1)	34		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	4.5		V/ns
P_D	Power Dissipation (TC = 25℃) -Derate above 25℃	30 0.8	30 0.8	W W/℃
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150		℃
T_L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300		℃

* Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	OSD5N60S	OSU5N60S	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.2	1.2	℃/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.5	0.5	℃/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62	62	℃/W

OSD5N60S/OSU5N60S

Electrical Characteristics TC = 25℃ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off Characteristics						
BVDSS	Drain-Source Breakdown Voltage	VGS = 0V, ID = 250μA, TJ = 25℃	600	--	--	V
		VGS = 0V, ID = 250μA, TJ = 150℃	--	650	--	V
ΔBVDSS / ΔTJ	Breakdown Voltage Temperature Coefficient	ID = 250μA, Referenced to 25℃	--	0.6	--	V/℃
IDSS	Zero Gate Voltage Drain Current	VDS = 600V, VGS = 0V VDS =480V, TC = 125℃	--	--	1 10	μA μA
IGTSF	Gate-Body Leakage Current, Forward	VGS = 30V, VDS = 0V	--	--	100	nA
IGSSR	Gate-Body Leakage Current, Reverse	VGS = -30V, VDS = 0V	--	--	-100	nA
On Characteristics						
VGS(th)	Gate Threshold Voltage	VDS = VGS, ID = 250μA	2.5	--	4.5	V
RDS(on)	Static Drain-Source On-Resistance	VGS = 10V, ID = 2.5A	--	0.77	0.85	Ω
gFS	Forward Transconductance	VDS = 40V, ID =2.5A (Note 4)	--	8	--	S
Rg	Gate Resistance	F=1MHz, open drain	--	3.5	--	Ω
Dynamic Characteristics						
Ciss	Input Capacitance	VDS = 25V, VGS = 0V, f = 1.0MHz	--	320	--	pF
Coss	Output Capacitance		--	75	--	pF
Crss	Reverse Transfer Capacitance		--	4	--	pF
Switching Characteristics						
td(on)	Turn-On Delay Time	VDD = 400V, ID = 2.5A RG = 20Ω (Note 4, 5)	--	18	--	ns
tr	Turn-On Rise Time		--	40	--	ns
td(off)	Turn-Off Delay Time		--	50	--	ns
tf	Turn-Off Fall Time		--	30	--	ns
Qg	Total Gate Charge	VDS = 480V, ID = 5A VGS = 10V (Note 4, 5)	--	15	--	nC
Qgs	Gate-Source Charge		--	3	--	nC
Qgd	Gate-Drain Charge		--	6	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
IS	Maximum Continuous Drain-Source Diode Forward Current		--	--	5	A
ISM	Maximum Pulsed Drain-Source Diode Forward Current		--	--	20	A
VSD	Drain-Source Diode Forward Voltage	VGS = 0V, IS = 5A	--	--	1.5	V
trr	Reverse Recovery Time	VGS = 0V, IS = 5A dIF/dt =100A/μs (Note 4)	--	180	--	ns
Qrr	Reverse Recovery Charge		--	2.5	--	μC

NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. L=60mH, I_{AS}=1.5A, VDD=150V, Starting TJ=25 ℃

3. I_{SD}≤4.5A, di/dt ≤ 200A/us, V_{DD} ≤ BV_{DSS}, Starting TJ = 25 ℃

4. Pulse Test: Pulse width ≤ 300us, Duty Cycle ≤ 2%

5. Essentially Independent of Operating Temperature Typical Characteristics

OSD5N60S/OSU5N60S

Typical Performance Characteristics

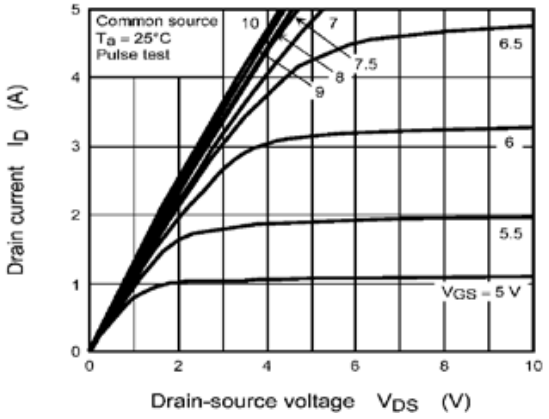


Figure 1: On-Region Characteristics@25° C

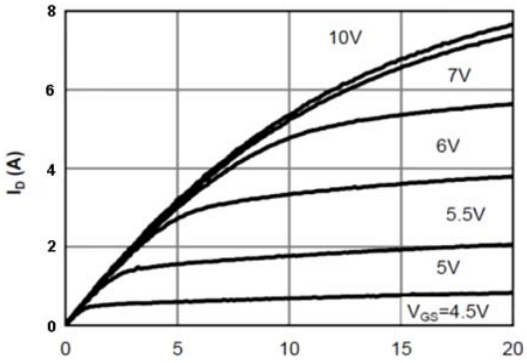


Figure 2: On-Region Characteristics@25° C

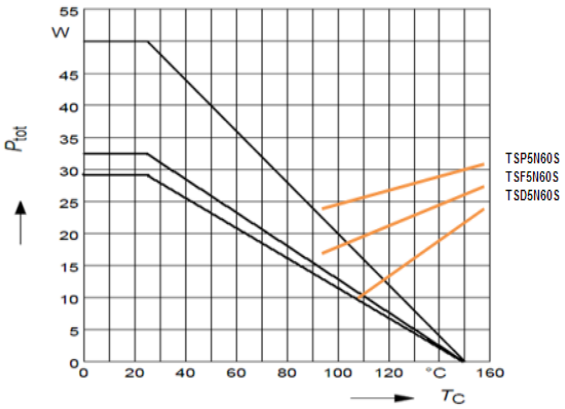


Figure 3: Power Dissipation

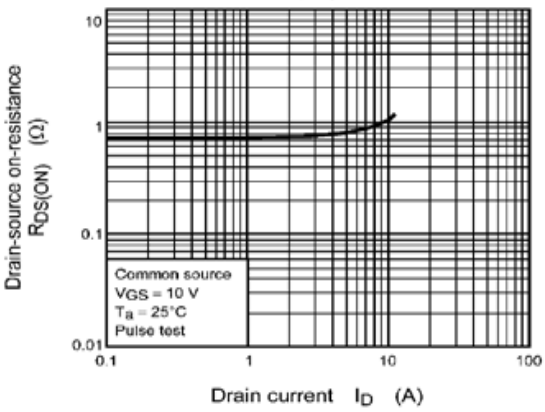


Figure 4: On-Resistance vs. Drain Current and Gate Voltage

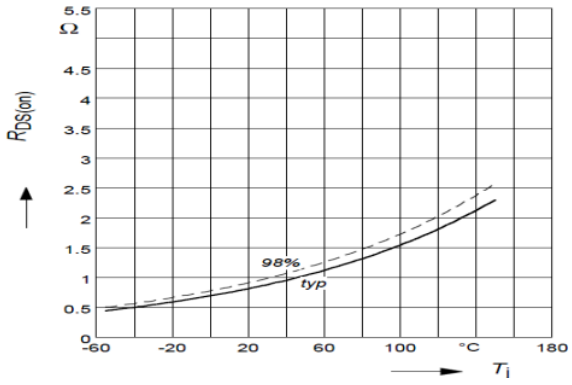


Figure 5: On-Resistance vs. Junction Temperature

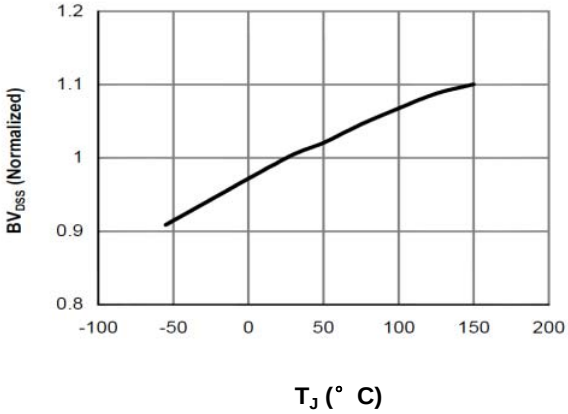


Figure 6: Break Down vs. Junction Temperature

Typical Performance Characteristics

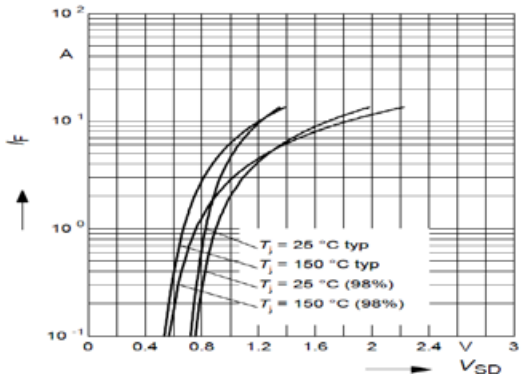


Figure 7: Body-Diode Characteristics

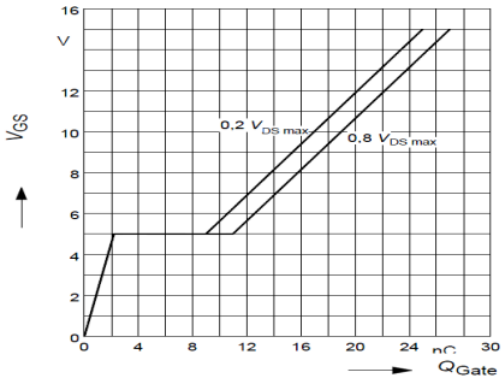


Figure 8: Gate-Charge Characteristics

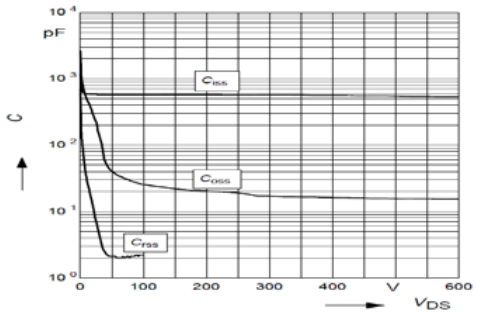


Figure 9: Capacitance Characteristics

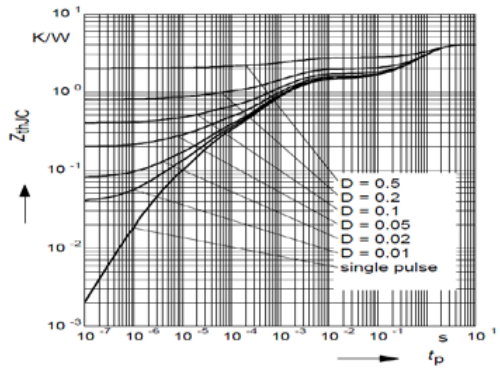


Figure 10: C_{oss} stored Energy

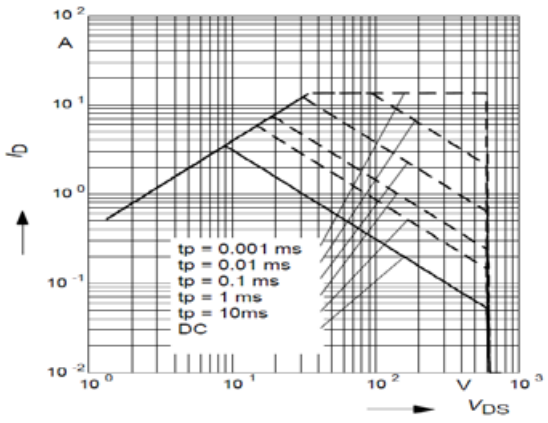


Figure 11: Maximum Forward Biased Safe Operating Area (Full PAK)

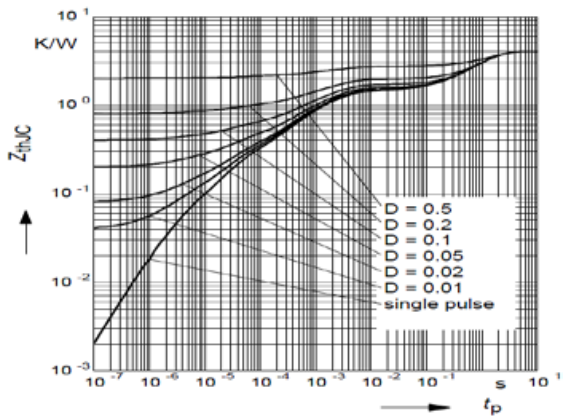
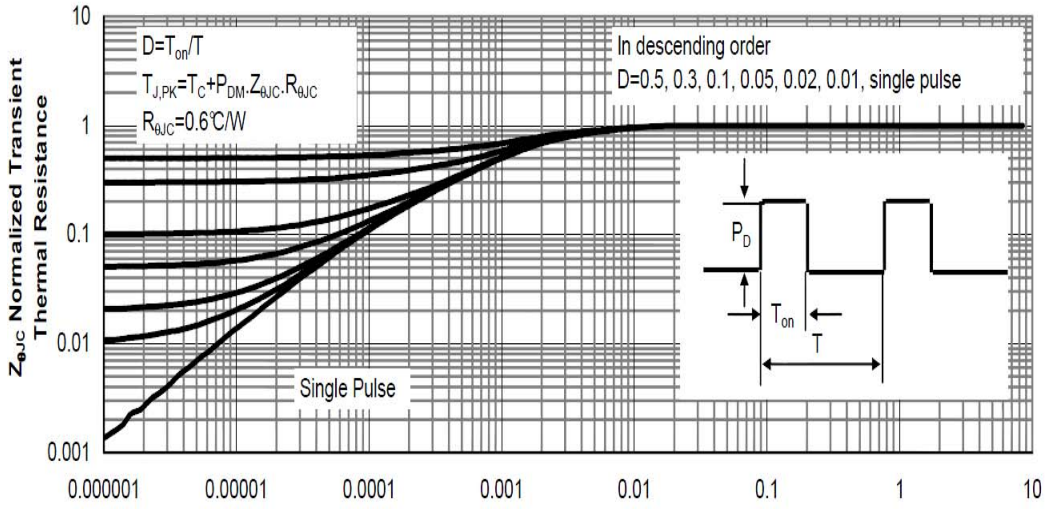


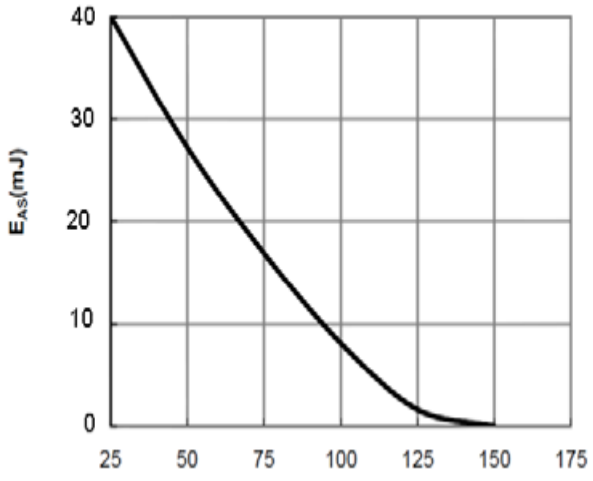
Figure 12: Single Pulse Power Rating Junction to Case (Full PAK)

Typical Performance Characteristics



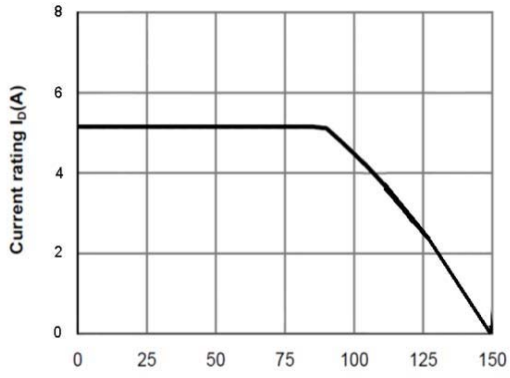
Pulse Width (s)

Figure 12: Normalized Maximum Transient Thermal Impedance



T_{CASE} (° C)

Figure 13: Avalanche energy



T_{CASE} (° C)

Figure 14: Current De-rating

Typical Performance Characteristics

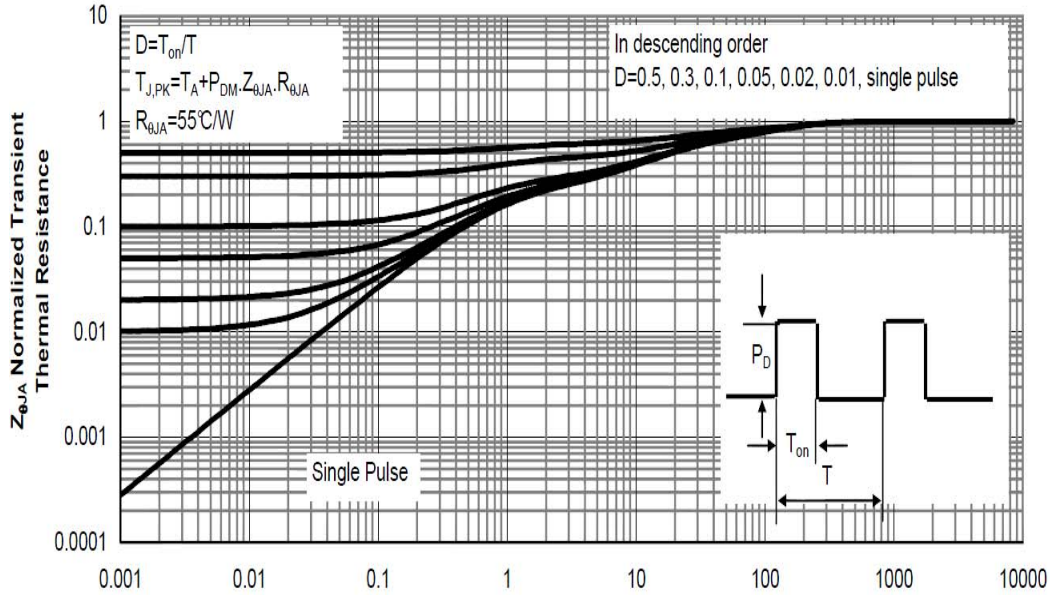


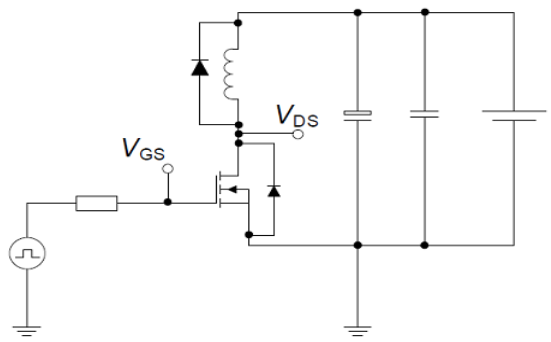
Figure 15: Normalized Maximum Transient Thermal Impedance

OSD5N60S/OSU5N60S

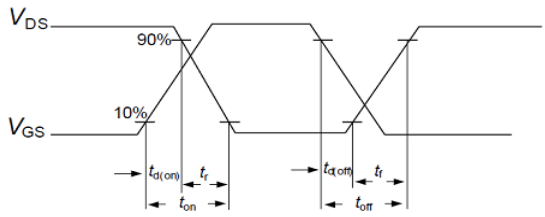
Test circuits

Switching times test circuit and waveform for inductive load

Switching times test circuit for inductive load

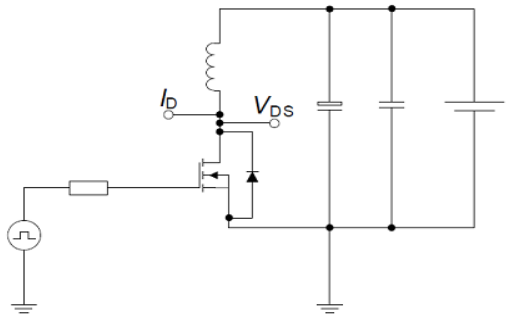


Switching time waveform

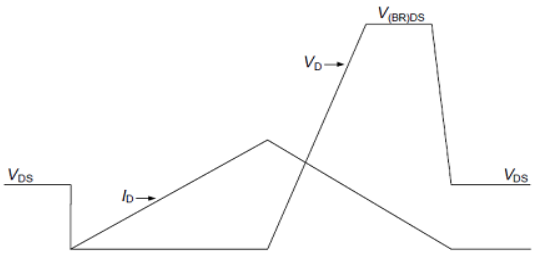


Unclamped inductive load test circuit and waveform

Unclamped inductive load test circuit

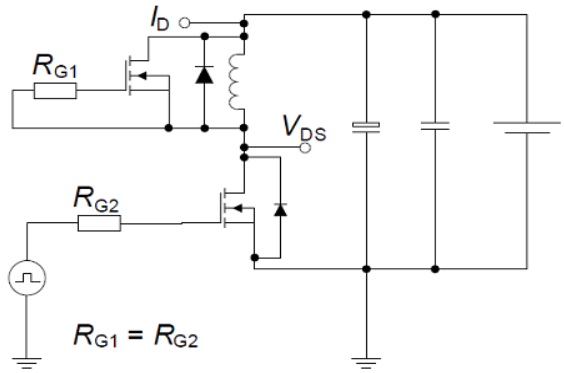


Unclamped inductive waveform

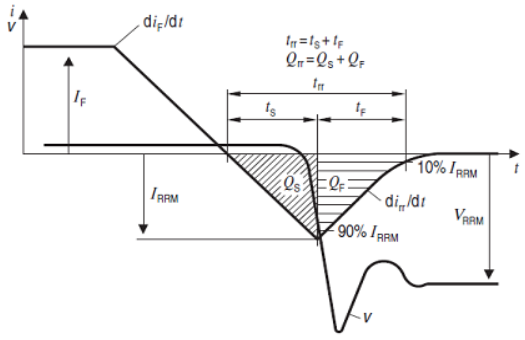


Test circuit and waveform for diode characteristics

Test circuit for diode characteristics

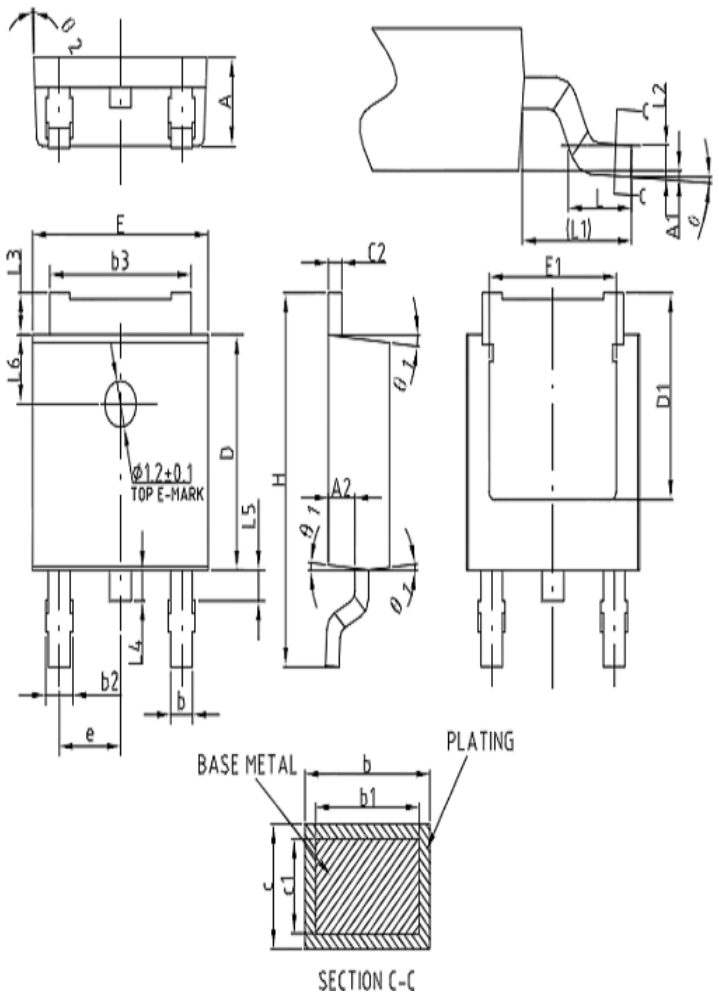


Diode recovery waveform



OSD5N60S/OSU5N60S

PKG TO-252



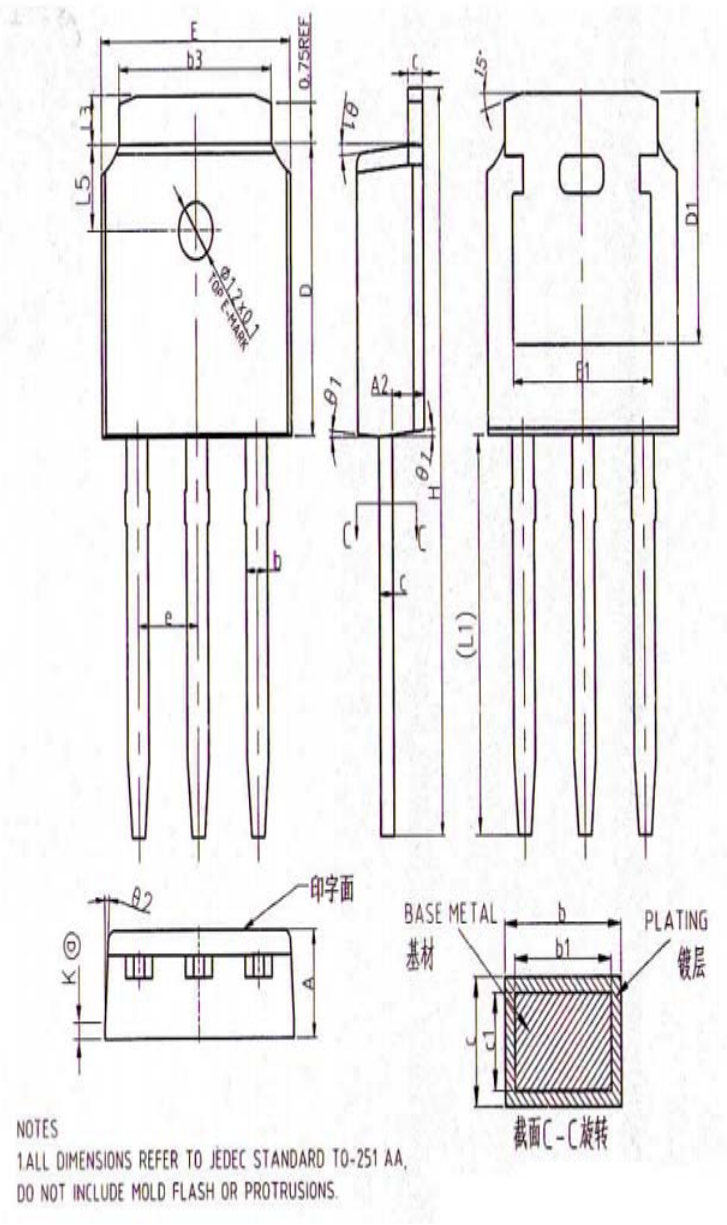
COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0	–	0.10
A2	0.90	1.00	1.10
b	0.77	–	0.89
b1	0.76	0.81	0.86
b2	0.77	–	1.10
b3	5.23	5.33	5.43
c	0.47	–	0.60
c1	0.46	0.51	0.56
c2	0.47	–	0.60
D	6.00	6.10	6.20
D1	5.25	–	–
E	6.50	6.60	6.70
E1	4.70	–	–
e	2.28BSC		
H	9.80	10.10	10.40
L	1.40	1.50	1
L1	2.90REF		
L2	0.51BSC		
L3	0.90	–	1.25
L4	0.60	0.80	1.00
⚠4 L5	0.90	–	1.50
⚠4 L6	1.80REF		
θ	0°	–	8°
θ1	3°	5°	7°
θ2	1°	3°	5°

NOTES:
ALL DIMENSIONS REFER TO JEDEC STANDARD TO-252 AA DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.

OSD5N60S/OSU5N60S

PKG TO-251



COMMON DIMENSIONS

SYMBOL	MM		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A2	0.97	1.07	1.17
b	0.72	0.78	0.85
b1	0.71	0.76	0.81
b3	5.23	5.33	5.46
c	0.47	0.53	0.58
c1	0.46	0.51	0.56
D	6.00	6.10	6.20
D1	5.30REF		
E	6.50	6.60	6.70
E1	4.70	4.83	4.92
e	2.286BSC		
H	16.10	16.40	16.60
L1	9.20	9.40	9.60
L3	0.90	1.02	1.25
L5	1.70	1.80	1.90
θ1	5°	7°	9°
θ2	5°	7°	9°
K	0.40REF		